	Application No.	Applicant(a)
	Application No.	Applicant(s)
Notice of Allowability	09/909,057	PARK, COLLIN Y.
Notice of Anomability	Examiner	Art Unit
	Trent J Roche	2124
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to <u>communications filed 19 July 2001</u> .		
2. The allowed claim(s) is/are <u>1-20</u> .		
3. The drawings filed on 19 July 2001 are accepted by the Examiner.		
4.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 98), 7. ☐ Examiner's Amenda	e

DETAILED ACTION

- 1. This office action is responsive to communications filed 19 July 2001.
- 2. Claims 1-20 have been examined.

Allowable Subject Matter and Examiner's Statement of Reason(s) for Allowance

- 3. Claims 1-20 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

The closest prior art of record, specifically U.S. Patent Publication 2002/0184613 A1 to Kuzemchak et al., taken alone or in combination, fails to teach or suggest a method and computer readable medium for testing a software emulator in such a manner as recited in independent claims 1, 9 and 16.

Instead, Kuzemchak et al. discloses a method for verifying the correct execution of an application that has been ported from one instruction set architecture (ISA) to another ISA. This is accomplished by preparing versions of the application for the source ISA and for the target ISA. These versions are then executed on a simulator or emulator for the appropriate ISA, and after execution is finished, results of the two executions are compared to determine their equivalency and the validity of the ported application. However, Kuzemchak et al. does not explicitly disclose the steps required by independent claims 1, 9 and 16. Specifically, Kuzemchak et al. does not explicitly disclose executing a test program on a target machine architecture, whereby a test program produces a first output; executing an emulator on the target machine architecture; and the emulator executing the test program under emulation, whereby the test program produces a second output as claimed, as Kuzemchak et al. discloses building two separate versions of a test program, one version specific to a source ISA, and one

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version specific to a target ISA. This is in contrast to the instant application, wherein a single test program is executed on a target machine architecture, then executing the same test program on an emulator running on the target machine architecture, as required by claim 1. Similar limitations are recited in independent claims 9 and 16.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Trent J Roche whose telephone number is (571)272-3733. The examiner can normally be reached on Monday - Friday, 9:00 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki can be reached on (571)272-3719. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Trent J Roche Examiner Art Unit 2124

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ANIL KHATRI
PRIMARY EXAMINER